

## PATENT APPLICATION

042390.P0744C2

**AMENDMENT****Amendment to Claims**

Please add the claims as shown below.

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33. (Newly added) An apparatus comprising:  
a machine readable storage medium having stored thereon instructions capable of being executed by a data processing platform, said instructions being adapted to:

encode an instruction to provide a corresponding executable address.

34. (Newly added) The apparatus of claim 33, wherein said instructions are further adapted to receive the instruction.

35. (Newly added) The apparatus of claim 33, wherein said instructions are further adapted to generate an execution stream.

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36. (Newly added) A method comprising:  
translating a source code instruction to generate a subroutine address.

37. (Newly added) The method of claim 36, wherein translating the source code instruction includes directly translating the source code.

38. (Newly added) The method of claim 36, wherein translating the source code instruction includes translating the source code without generating an op code.

39. (Newly added) The method of claim 36, further comprising receiving the a source code instruction.

40. (Newly added) The method of claim 36, wherein translating the source code instruction includes parsing the source code instruction.

## PATENT APPLICATION

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21. (Newly added) An apparatus comprising:  
a machine readable storage medium having stored thereon instructions  
capable of being executed by a data processing platform, said  
instructions being adapted to:

receive a macroinstruction;  
encode said literal source code macroinstruction into a  
corresponding subroutine address;  
generate an execution stream; and  
store the subroutine address.

22. (Newly added) The apparatus of claim 21, wherein said instructions  
are further adapted to execute a subroutine identified by said subroutine  
address.

23. (Newly added) The apparatus of claim 22, wherein said instructions  
are further adapted to push an argument onto a stack, said argument adapted  
to be used as an input to said subroutine identified by said subroutine address.

24. (Newly added) The apparatus of claim 22, wherein said instructions  
are further adapted to pop an argument from a stack, said argument adapted  
to be used as an input to said subroutine identified by said subroutine address.

25. (Newly added) The apparatus of claim 22, wherein said instructions  
are further adapted to push a result of the execution of said subroutine onto a  
stack.

26. (Newly added) The apparatus of claim 22, wherein said instructions  
are further adapted to point to the first item associated with said subroutine  
stored in said execution stream.

27. (Newly added) The apparatus of claim 21, wherein said instructions  
are further adapted to recursively execute a subroutine.